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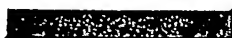
- ☐
1. An evaluation of checklist-based reading for entity-relationship diagrams
Wohlin, C.; Aurum, A.;
Software Metrics Symposium, 2003. Proceedings. Ninth International
3-5 Sept. 2003 Page(s):286 - 296
AbstractPlus | Full Text: PDF(277 KB) IEEE CNF

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7. A 70 mW 7th-order filter with 7 to 50 MHz cutoff frequency, programmable boost, and group-delay eq
Rezzi, F.; Cazzaniga, M.; Bietti, L.; Castello, R.;
Solid-State Circuits Conference, 1997. Digest of Technical Papers. 44th ISSCC., 1997 IEEE International
6-8 Feb. 1997 Page(s):60 - 61, 430
[AbstractPlus](#) | [Full Text: PDF\(972 KB\)](#) IEEE CNF
8. A 110 MHz 350 mW 0.6 μ CMOS 16-state generalized-target Viterbi detector for disk drive read channe
Sridharan, S.; Carley, L.R.;
Custom Integrated Circuits, 1999. Proceedings of the IEEE 1999
16-19 May 1999 Page(s):325 - 328
[AbstractPlus](#) | [Full Text: PDF\(392 KB\)](#) IEEE CNF
9. Simple detection technique for d=2 modulated optical recording channels
Gopalaswamy, S.; Farhang-Boroujeny, B.; Wilson, W.Y.H.;
Communications, 1999. ICC '99. 1999 IEEE International Conference on
Volume 3, 6-10 June 1999 Page(s):1967 - 1971 vol.3
[AbstractPlus](#) | [Full Text: PDF\(364 KB\)](#) IEEE CNF



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- ☐ 1. Multi-level decision feedback equalization: an efficient realization of FDS/DF
 Kenney, J.G.; Wood, R.;
 Magnetics, IEEE Transactions on
 Volume 31, Issue 2, Mar 1995 Page(s):1115 - 1120
[AbstractPlus](#) | Full Text: [PDF\(572 KB\)](#) IEEE JNL
- ☐ 2. Advanced channels for DVD with d=2 modulation coding
 Gopalaswamy, S.; Wood, R.;
 Optical Data Storage Topical Meeting, 1997. ODS. Conference Digest
 7-9 April 1997 Page(s):40 - 41
[AbstractPlus](#) | Full Text: [PDF\(124 KB\)](#) IEEE CNF
- ☐ 3. A 300 Mb/s BICMOS disk drive channel with adaptive analog equalizer
 Bishop, A.; Chan, I.; Aronson, S.; Moran, P.; Hen, K.; Cheng, R.; Fitzpatrick, K.K.; Stander, J.; Chik, R.; Ksho
 Ngai, J.; He, H.; daVeiga, E.; Botte, P.; Krasuk, C.; Cerqua, B.; Brown, R.; Ziperovich, P.; Fisher, K.;
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 15-17 Feb. 1999 Page(s):46 - 49
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- ☐ 4. Spectral shaping for peak detection equalization
 Thapar, H.K.; Sands, N.P.; Abbott, W.L.; Cioffi, J.M.;
 Magnetics, IEEE Transactions on
 Volume 26, Issue 5, Sep 1990 Page(s):2309 - 2311
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- ☐ 5. A 110 MHz 350 mW 0.8 μ m CMOS 16-state generalized-target Viterbi detector for disk drive read chan
 Sridharan, S.; Carley, L.R.;
 Solid-State Circuits, IEEE Journal of
 Volume 35, Issue 3, March 2000 Page(s):362 - 370
[AbstractPlus](#) | [References](#) | Full Text: [PDF\(292 KB\)](#) IEEE JNL
- ☐ 6. Fully-Integrated 5 to 15 MHz programmable bipolar Bessel lowpass filter
 De Veirman, G.A.; Yamasaki, R.G.;
 Circuits and Systems, 1990., IEEE International Symposium on
 1-3 May 1990 Page(s):1155 - 1158 vol.2
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1. A 72 Mb/s PRML disk-drive channel chip with an analog sampled-data signal processor

Yamasaki, R.G.; Pan, T.; Palmer, M.; Browning, D.;

Solid-State Circuits Conference, 1994. Digest of Technical Papers. 41st ISSCC., 1994 IEEE International 16-18 Feb. 1994 Page(s):278 - 279

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2. Improving performance of PRML/EPRML through noise prediction

Eleftheriou, E.; Hirt, W.;

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Volume 32, Issue 5, Sept. 1996 Page(s):3968 - 3970

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1. Performance benchmarks for an interactive video-on-demand system
Guo, J.; Taylor, P.G.; Wong, E.W.M.; Chan, S.; Zukerman, M.; Tang, K.S.;
Communications, 2004 IEEE International Conference on
Volume 4, 20-24 June 2004 Page(s):2189 - 2193 Vol.4
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